☐ Performance Specification – J series

| ITEM | | J2 | J1 |
|---|---|---|---|
| Operating control method | | Cyclic operation by stored program | |
| I/O control method | | Batch processing method (when END instruction is executed) | |
| Operation time | | Basic instruction 0.5us, Applied instruction from 2us to several 100us. | |
| Programming language | | Relay symbolic language + Step ladder | |
| Program capacity / memory | | 16000 steps ( built in EEprom ) | 8000 steps ( built in EEprom ) |
| Number of instruction | | Basic instruction:27, Step ladder instruction:2, Applied instruction:107 | |
| Input Relay | | X000 ~ X177 (Sink/Source DC24V 7mA photo coupler isolation) | |
| Output Relay | | Y000 ~ Y177 (Relay : AC250V/1A or Transistor : DC30V/0.5A) | |
| Auxiliary Relay (M) | Latched | M000 ~ M499 ( EEprom backup ) | |
| | General | M500 ~ M3071 (no backup) | M500 ~ M1535 (no backup) |
| | Special | M8000 ~ M8255 (no backup) | |
| State Relay (S) | Latched | S000 ~ S499 ( EEprom backup ) | |
| | General | S500 ~ S999 (no backup) | |
| Timer (T) | 100 msec | T000 ~ T199 (no backup) | |
| | 10 msec | T200 ~ T245 (no backup) | |
| | 1 ms integration | 4 points, T246 ~ T249 (EEPROM backup) | |
| | 100 ms integration | 6 points, T250 ~ T255 (EEPROM backup) | |
| | Analog | 2 points (Defined by user) | |
| Counter (C) | 16bits Counter | Latched C00 ~ C31 (EEprom backup) | |
| | | General C32 ~ C199 | |
| | 32bits Counter | General C200 ~ C215 | |
| | | Latched C216 ~ C255 (EEprom backup) | |
| | HighSpeedCounter 100KHz | J2n32M(T)R : 1 phase1count (8)points X0~X7 2phase2count -ABphase(x4) J22(4)A,J22(4)B :1 phase1count (6)points X0~X5 2phase2count -ABphase(x3) | J1n32M(T)R : 1 phase1count (6)points X0~X5 2phase2count -ABphase(x2) |
| High Speed Pulse Out (200Kpps) | | J2n32M(T)R : 0 point J22A(B)32MT : 2 points, J24A(B)32MT : 4 points | None |
| Pseudo Axis | | J22A(B)32MT 1 points,J24A(B)32MT 1 points | None |
| Multi Axis Moving | | J22A, J22B, J24A, J24B (J2n None) | None |
| Linear & Circular Interpolation | | J22B,J24B with any 2 axis interpolation | None |
| Data Register | Latched | D000 ~ D255 (EEprom backup) | |
| | General | D256 ~ D7999 (can used FNC(12) MOV stored at EEPROM) | |
| | Special | D8000 ~ D8255 (no backup) | |
| Index Pointer(V,Z) | | V0 ~ V7, Z0 ~ Z7 | |
| For-Next Pointer (N) | | N0 ~ N7 | |
| Subroutine Pointer (P) | | P000 ~ P127 (CJ,CALL) | |
| Pointer ( I ) Interrupt ( I ) | | I00x, I10x, I20x, I30x, I40x, I50x (external interrupt), x=1 rising edge, x=0 falling edge | |
| | | I8xx (timer interrupt), xx=01~99ms | |
| | | I010, I020, I030, I040, I050, I060 : High Speed Counter interrupt | |
| Communication Interface | | (COM1)RS-422 (COM2)option RS-232C/RS-422,RS-485 | |
| Calendar | (Option) | Week, Year, Month, Day, Hour, Minute, Second | |
| Constant(K) | Decimal | 16 bits: -32,768 ~ +32,767 | |
| | | 32 bits: -2,147,483,648 ~ +2,147,483,647 | |
| Constant(H) | Hexadecimal | 16 bits: 0000h ~ FFFFh | |
| | | 32 bits: 00000000h ~ FFFFFFFFh | |

◉1N-Type : J1n32MR, J1n32MT(Without high speed pulse output)
◉2N-Type : J2n32MR, J2n32MT(Without high speed pulse output) J22(4)A,J22(4)B(With high speed pulse output)

☐ General Specification

| Item | Description |
|---|---|
| Source Voltage | 100~240VAC   50/60 Hz |
| Supply current | 24VDC / 800 mA |
| Momentary power failure | Keep operation in 10 ms |
| Breakdown voltage | AC1500/1min (between output terminal and frame ground terminal) |
| Isolation resistance | DC500v/5mΩ |
| Noise Impedance | Noise voltage: 1000Vp-p, noise width: 1 us |
| Grounding | Class 3 ground |
| Ambient Temperature | 0 ~ 55°C |
| Ambient humidity | 35 ~ 85 RH (without condensation) |
| Atmosphere | Must be free from corrosive gasses |